## IN THE UNITED STATES DISTRICT COURT FOR THE WESTERN DISTRICT OF TEXAS WACO DIVISION

VLSI TECHNOLOGY LLC, Plaintiff,	
v.	Case No. 6:21-cv-00299-ADA
INTEL CORPORATION,  Defendant.	

<u>DEFENDANT INTEL CORPORATION'S RESPONSE TO</u> PLAINTIFF VLSI TECHNOLOGY LLC'S MOTION FOR ENTRY OF JUDGMENT On November 20, 2023, VLSI moved for entry of judgment and attached two proposed forms of judgment. Dkt. 642; Dkt. 642-1; Dkt. 642-2. VLSI's motion follows an April 2021 verdict in which a jury found that Intel does not infringe either U.S. Patent No. 6,633,187 ("the '187 Patent") or U.S. Patent No. 6,366,522 ("the '522 Patent") and that VLSI is entitled to no damages. Dkt. 549 at 2-3, 5. Although Intel does not oppose entry of judgment in Intel's favor, it submits this response because VLSI's motion is premature and suggests procedurally improper steps in certain respects.<sup>1</sup>

If the Court denies all post-trial motions, then Intel agrees with VLSI that the Court should enter final judgment of no infringement in Intel's favor. *See* Dkt. 642; *see* Dkt. 642-2. In that case, Intel proposes that the parties meet and confer on the form of final judgment after the Court denies the pending motions to ensure an orderly sequence of events.

If, however, the Court were inclined to grant any post-trial motions, then neither of VLSI's proposed judgments would be appropriate. For example, if the Court were to grant either of Intel's motions regarding invalidity or unenforceability of the asserted patents (Dkts. 599, 600), then the parties should meet and confer to propose a form of final judgment reflecting any such ruling. Or if the Court were to grant any of VLSI's post-trial motions on infringement or damages (though it should not),<sup>2</sup> then further proceedings on at least Intel's license defense (Dkt. 425-1) and any damages would be required. In that case, it would be premature to enter final judgment.

<sup>&</sup>lt;sup>1</sup> VLSI's motion also did not comply with the local rules because VLSI did not give prior notice to Intel that it intended to file the motion or ask to meet and confer. *See* L.R. CV-7(g).

<sup>&</sup>lt;sup>2</sup> As Intel has explained, Intel presented substantial evidence showing that the accused products do not satisfy two separate limitations for each asserted claim of each patent and thus the trial evidence was more than sufficient for the jury to reasonably find—as it did—that Intel's accused products do not infringe either patent. Dkt. 604.

Dated: December 4, 2023

Respectfully submitted,

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## /s/ Kelly Ransom

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## **CERTIFICATE OF SERVICE**

I hereby certify that all counsel of record are being served with a copy of the foregoing document via the Court's CM/ECF system on December 4, 2023.

/s/ Kelly Ransom Kelly Ransom